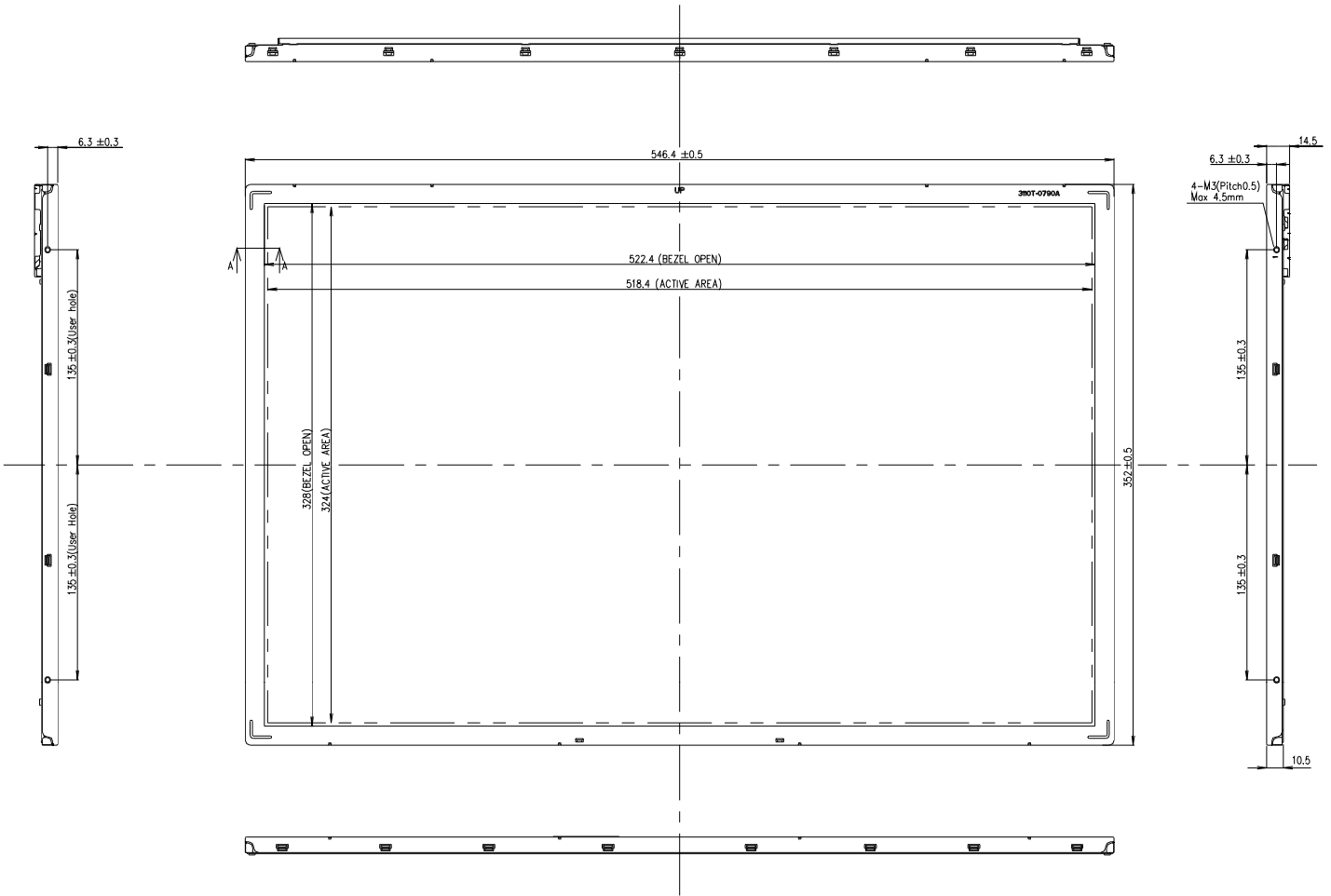


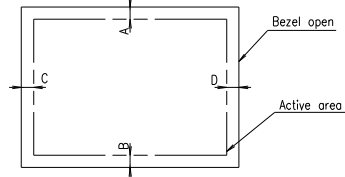
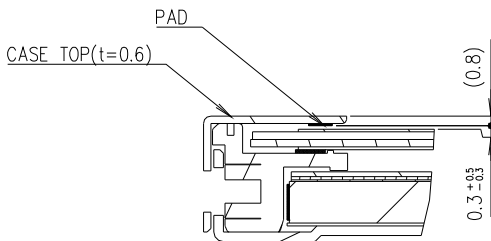
LANDMARK TECHNOLOGY: LMG240WUB-SLA2



Notes

- 1/F connector specification : GT103-30S-H23 (LSC)
2. LED connector specification : 10019HR-06PIN
3. Torque of user hole: 2.5~3.5kgf-cm.
4. Tilt and partial disposition tolerance of display area as following

- (1) Y-direction : $|A-B| \leq 1.4$
- (2) X-direction : $|C-D| \leq 1.4$



※PAD thickness : 0.2mm (+0.05/0)

5. Unspecified tolerances to be ± 0.5 mm
6. The COF area is weak & sensitive, so please don't press the COF area

- LCD Connector(CN1) : GT103-30S-H23-D (LSM), KDF71G-30S-1H(Hirose) or Equivalent
- Mating Connector : FI-X30C2L (Manufactured by JAE) or Equivalent

Pin No	Symbol	Description
1	RX00-	Minus signal of 1st channel 0 (LVDS)
2	RX00+	Plus signal of 1st channel 0 (LVDS)
3	RX01-	Minus signal of 1st channel 1 (LVDS)
4	RX01+	Plus signal of 1st channel 1 (LVDS)
5	RX02-	Minus signal of 1st channel 2 (LVDS)
6	RX02+	Plus signal of 1st channel 2 (LVDS)
7	GND	Ground
8	RXOC-	Minus signal of 1st clock channel (LVDS)
9	RXOC+	Plus signal of 1st clock channel (LVDS)
10	RX03-	Minus signal of 1st channel 3 (LVDS)
11	RX03+	Plus signal of 1st channel 3 (LVDS)
12	RXE0-	Minus signal of 2nd channel 0 (LVDS)
13	RXE0+	Plus signal of 2nd channel 0 (LVDS)
14	GND	Ground
15	RXE1-	Minus signal of 2nd channel 1 (LVDS)
16	RXE1+	Plus signal of 2nd channel 1 (LVDS)
17	GND	Ground
18	RXE2-	Minus signal of 2nd channel 2 (LVDS)
19	RXE2+	Plus signal of 2nd channel 2 (LVDS)
20	RXEC-	Minus signal of 2nd clock channel (LVDS)
21	RXEC+	Plus signal of 2nd clock channel (LVDS)
22	RXE3-	Minus signal of 2nd channel 3 (LVDS)
23	RXE3+	Plus signal of 2nd channel 3 (LVDS)
24	GND	Ground
25	NC	No Connection (For LCD internal use only.)
26	NC	No Connection (For LCD internal use only.)
27	PWM	PWM_OUT for Wavy Noise
28	V _{LCD}	Power Supply (12.0V)
29	V _{LCD}	Power Supply (12.0V)
30	V _{LCD}	Power Supply (12.0V)

First Pixel data

Second Pixel data